

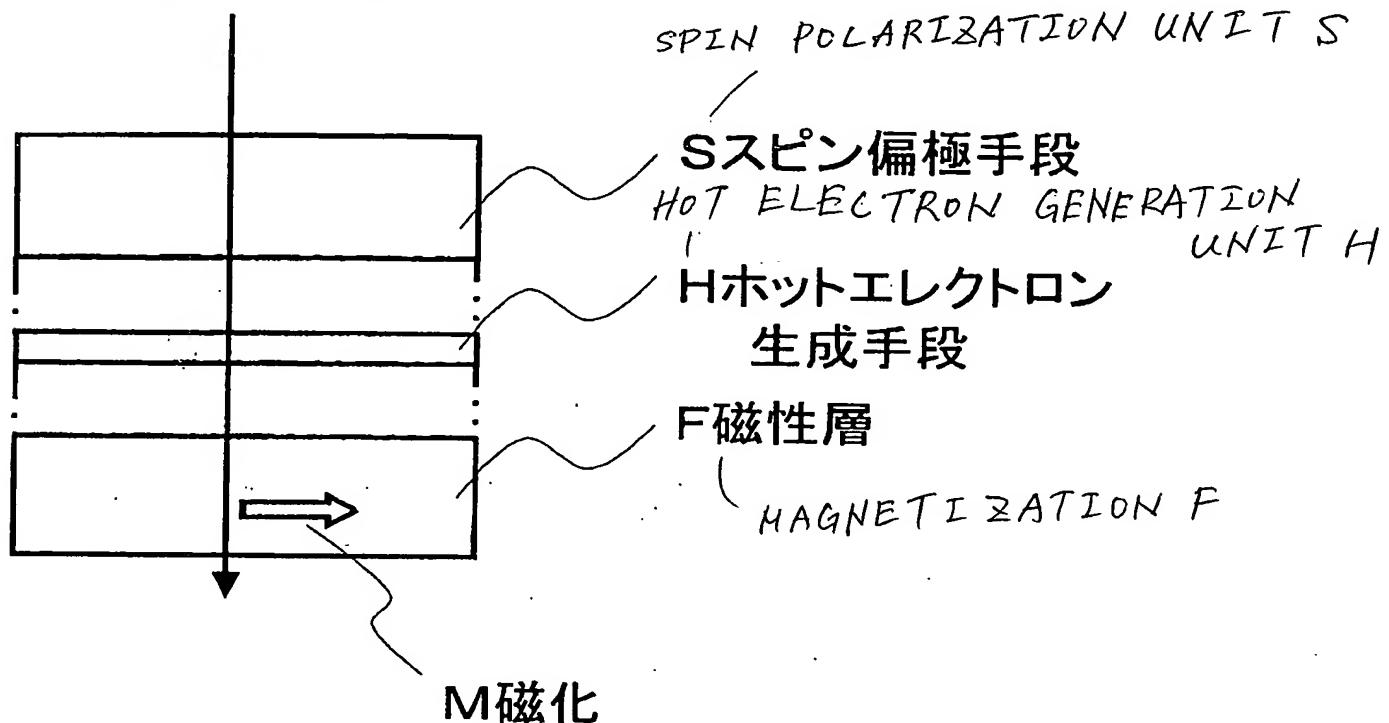
【書類名】図面

FIG. 1

[図1]

WRITE CURRENT I

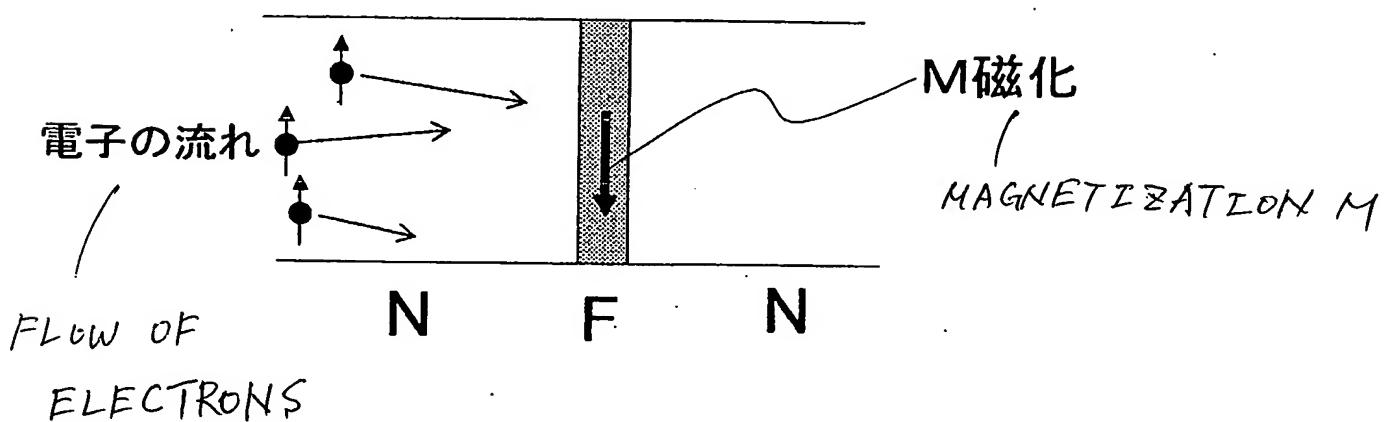
書き込み電流I



[図2]

CURRENT I'

電流 I'



[図3]

FIG. 3

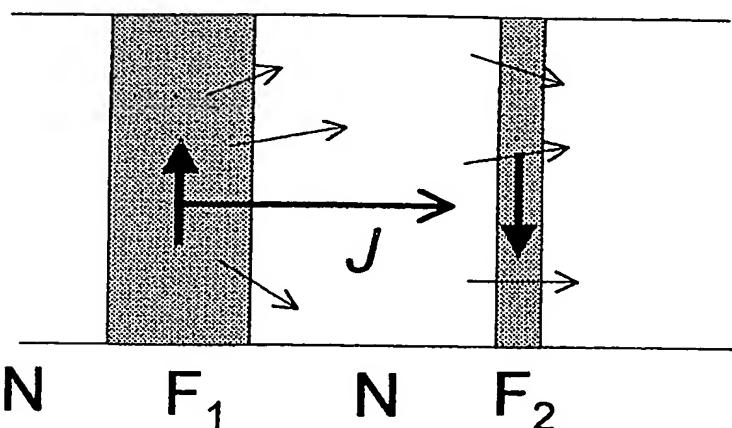
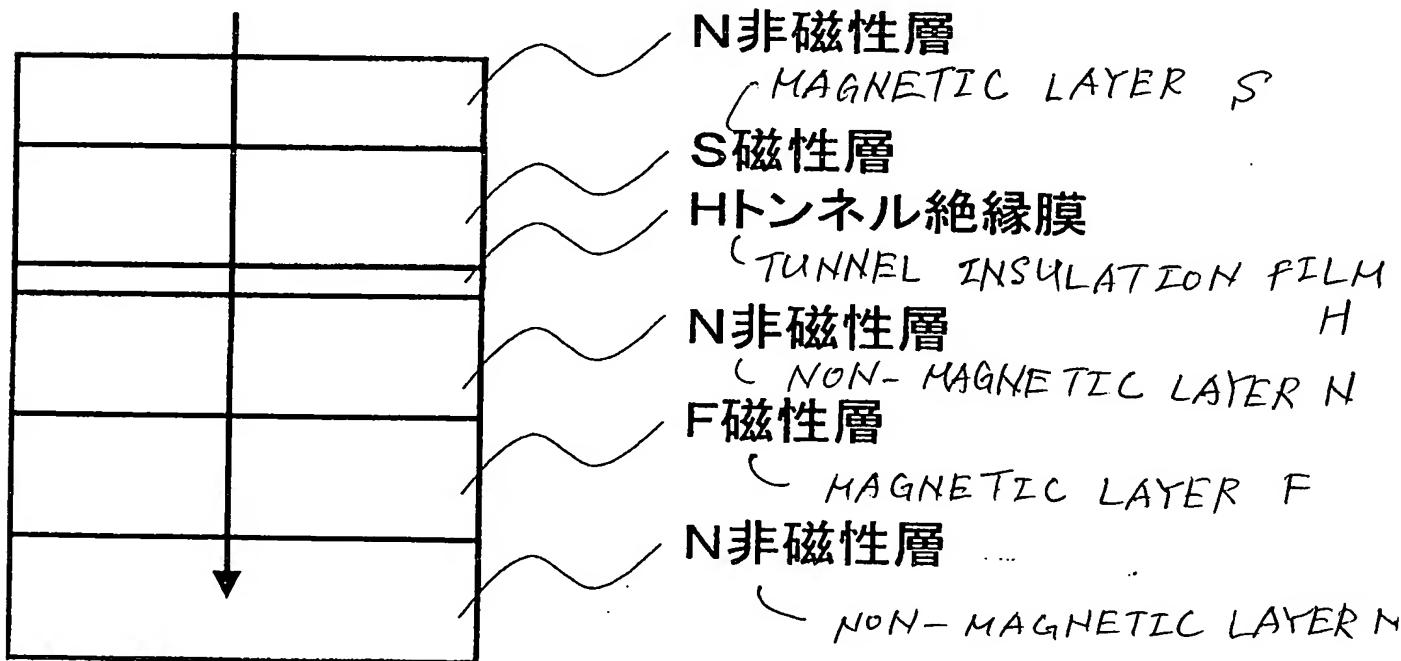


FIG. 4

[図4]

WRITE CURRENT I  
書き込み電流I

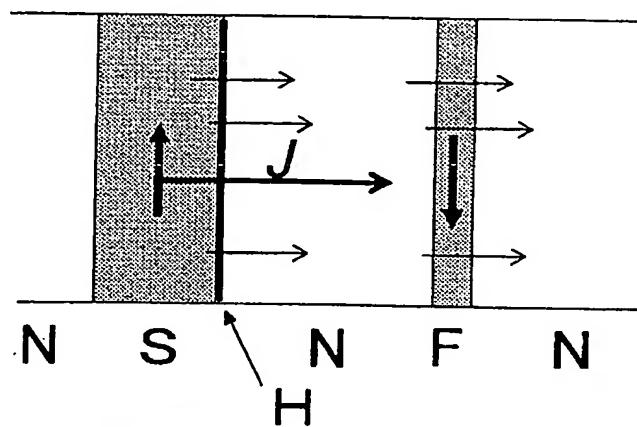
NON-MAGNETIC LAYER N



[図5]

FIG. 5A

(a)



(b)

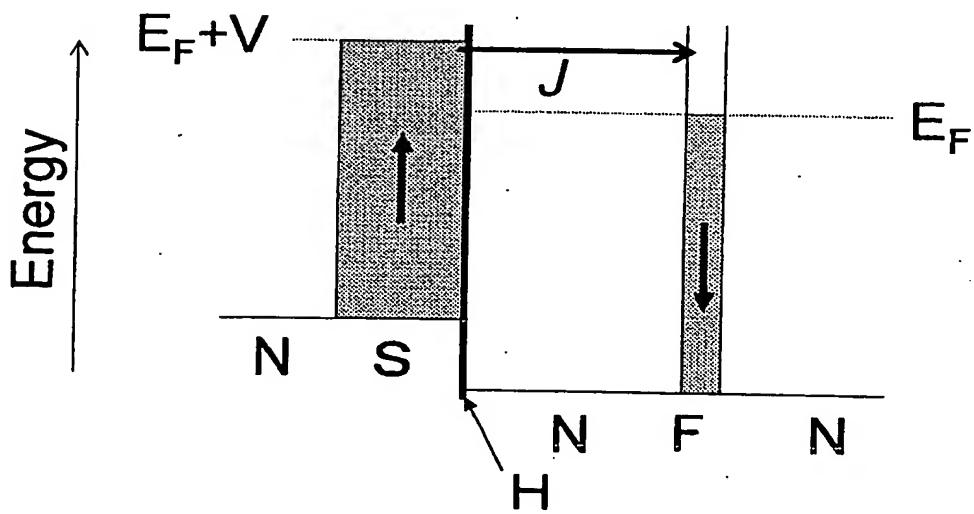


FIG. 5B

【図6】

FIG. 6

書き込み電流I —— WRITE CURRENT I

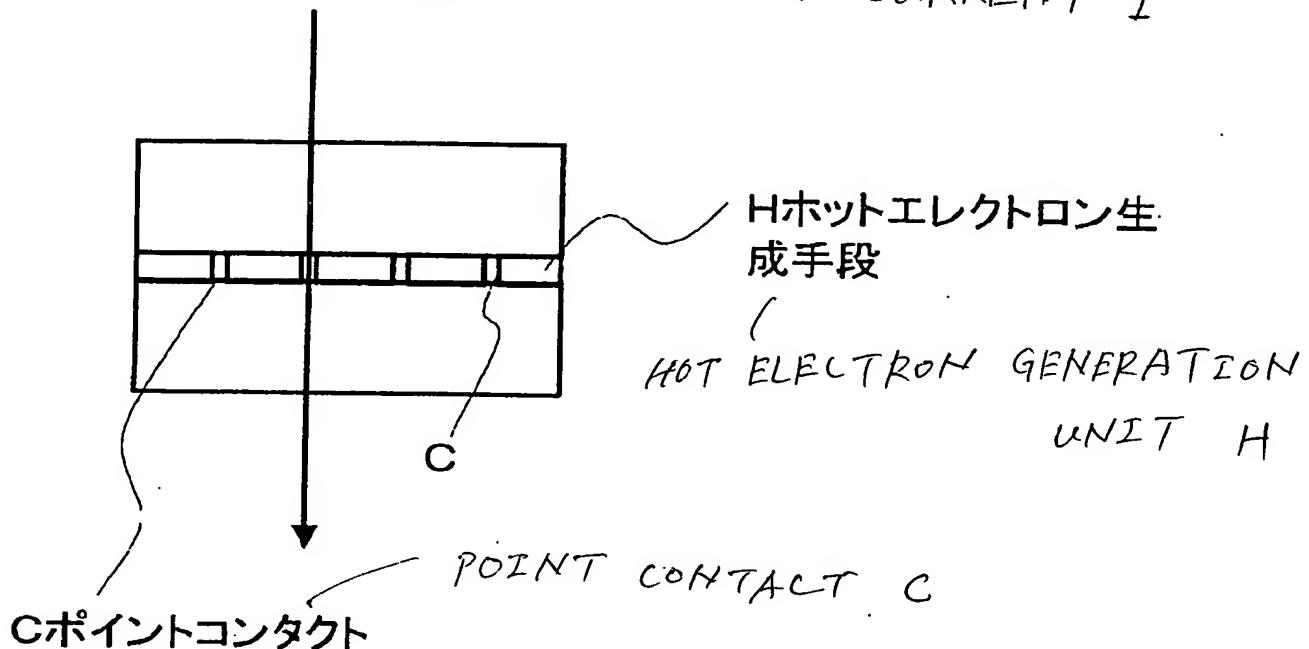
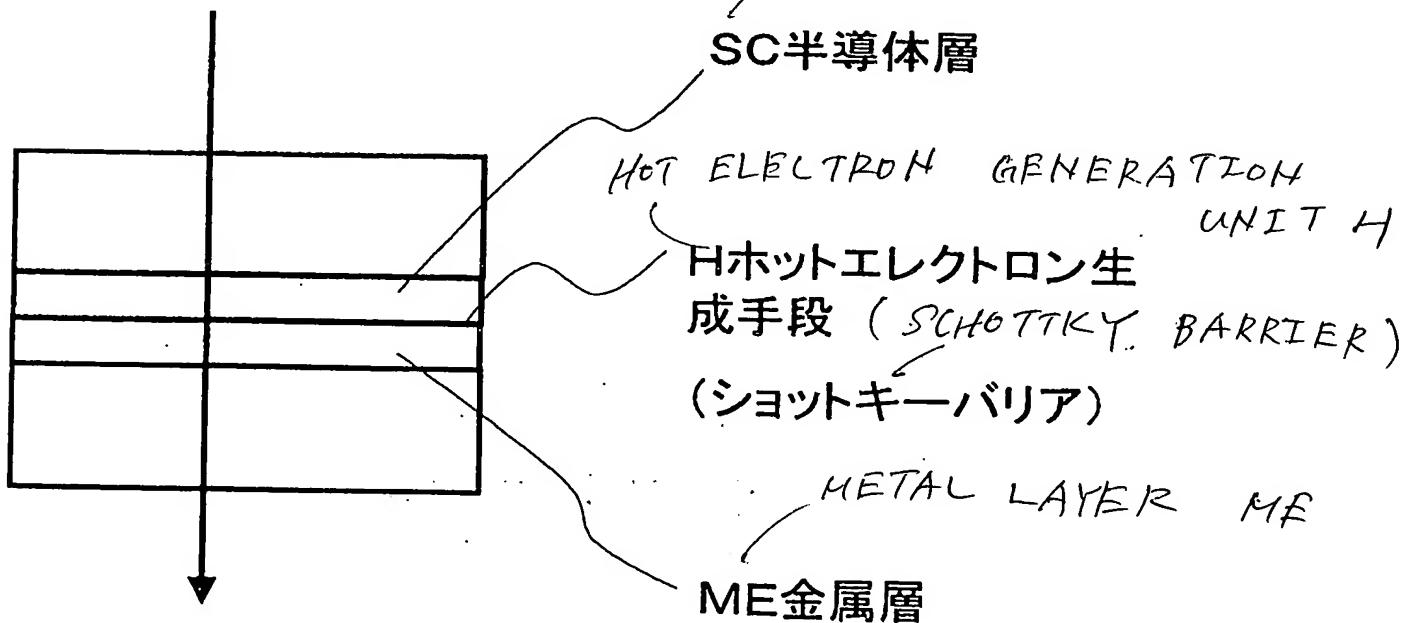


FIG. 7

【図7】

書き込み電流I —— WRITE CURRENT I

書き込み電流I SEMICONDUCTOR LAYER SC



[図8] FIG. 8A

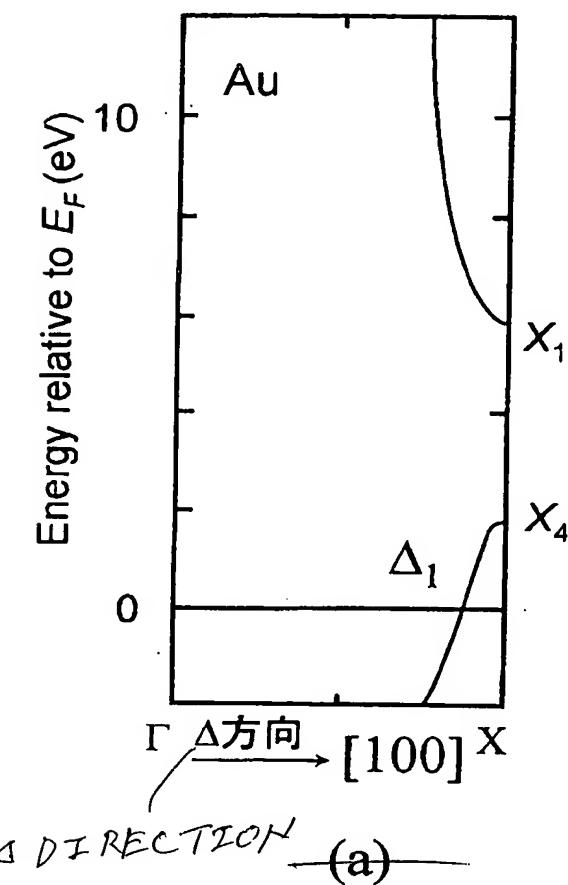
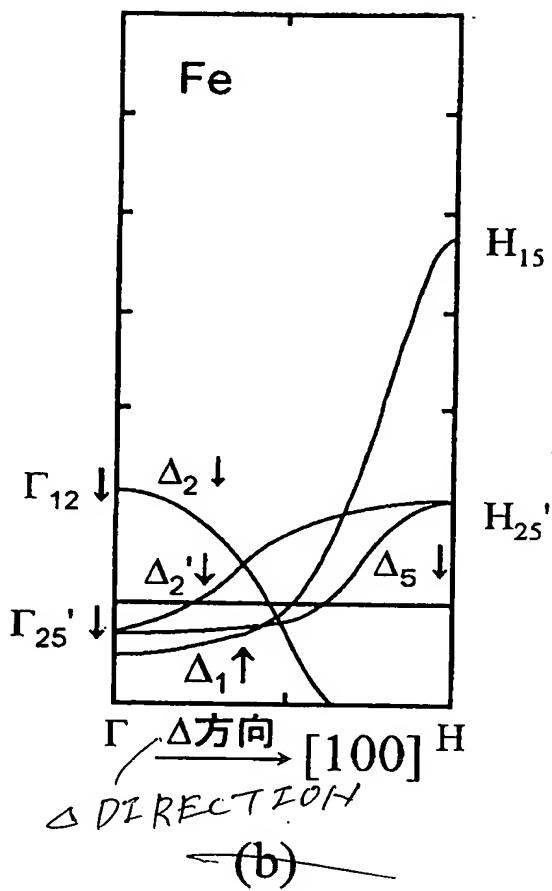
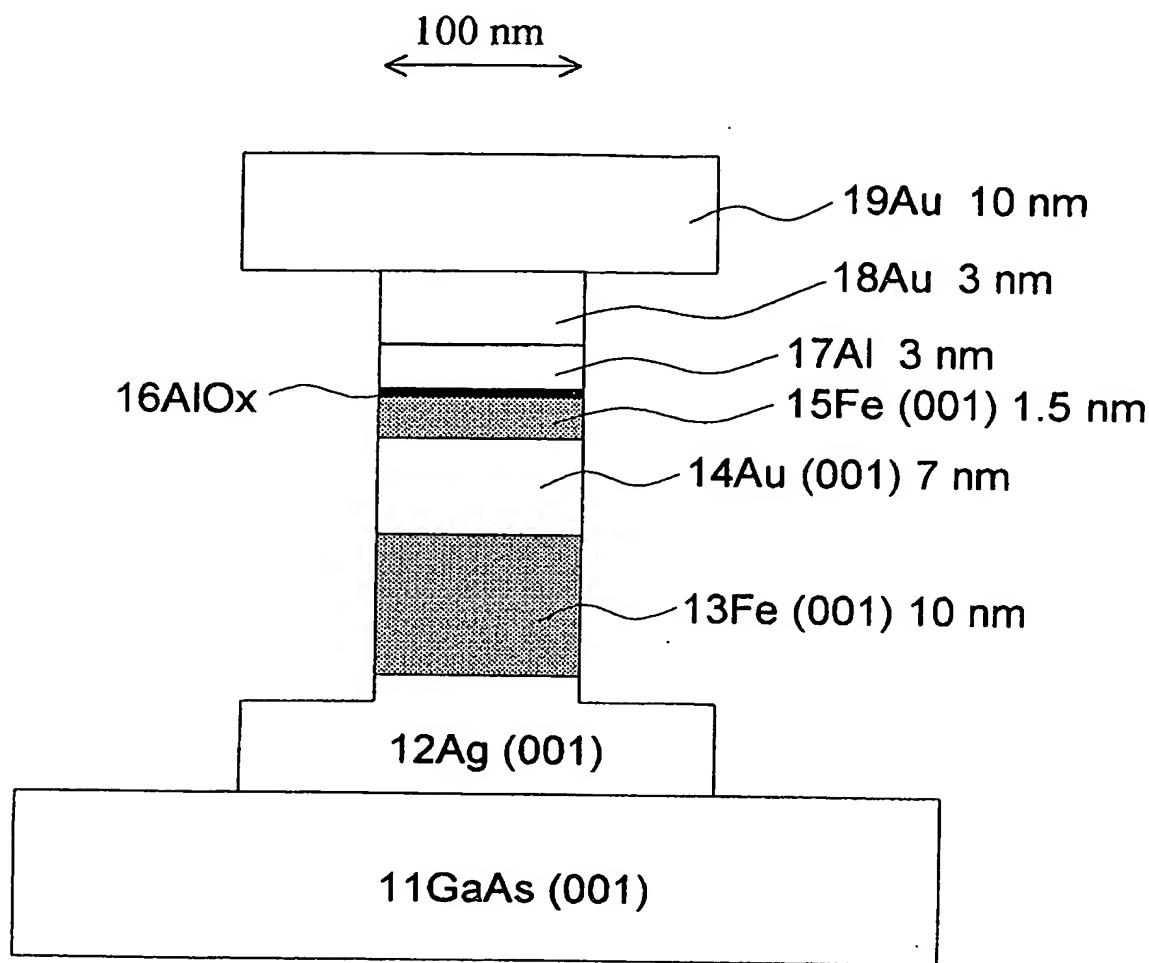


FIG. 8B



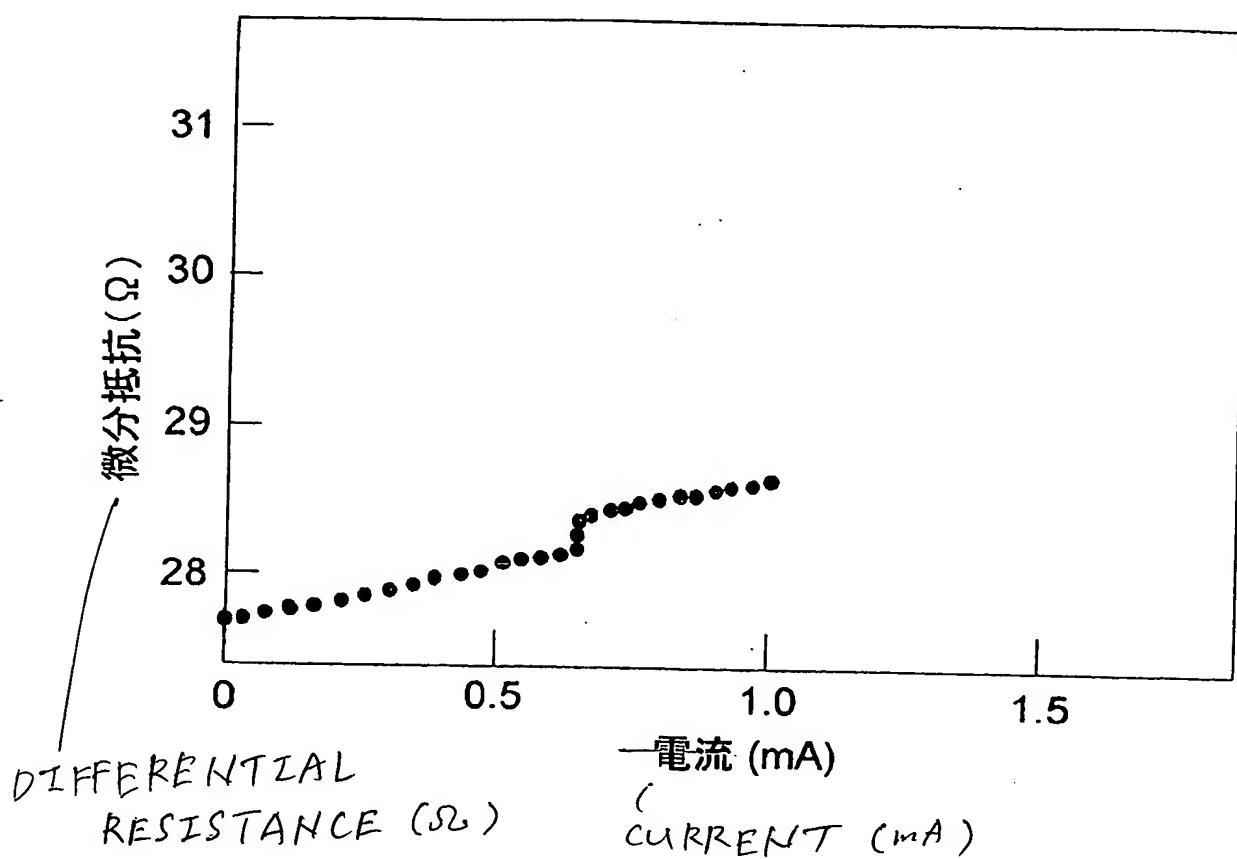
【図9】

FIG. 9



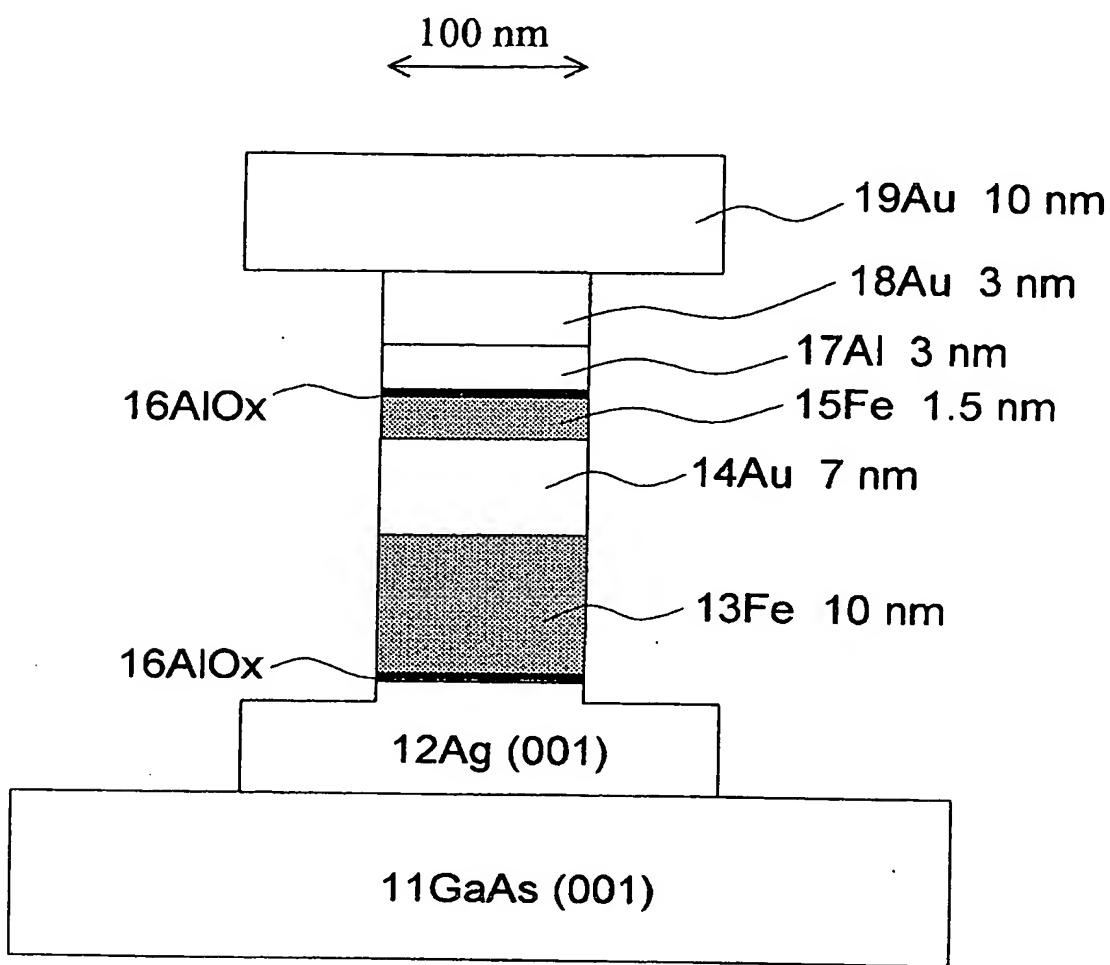
[図10]

FIG. 10



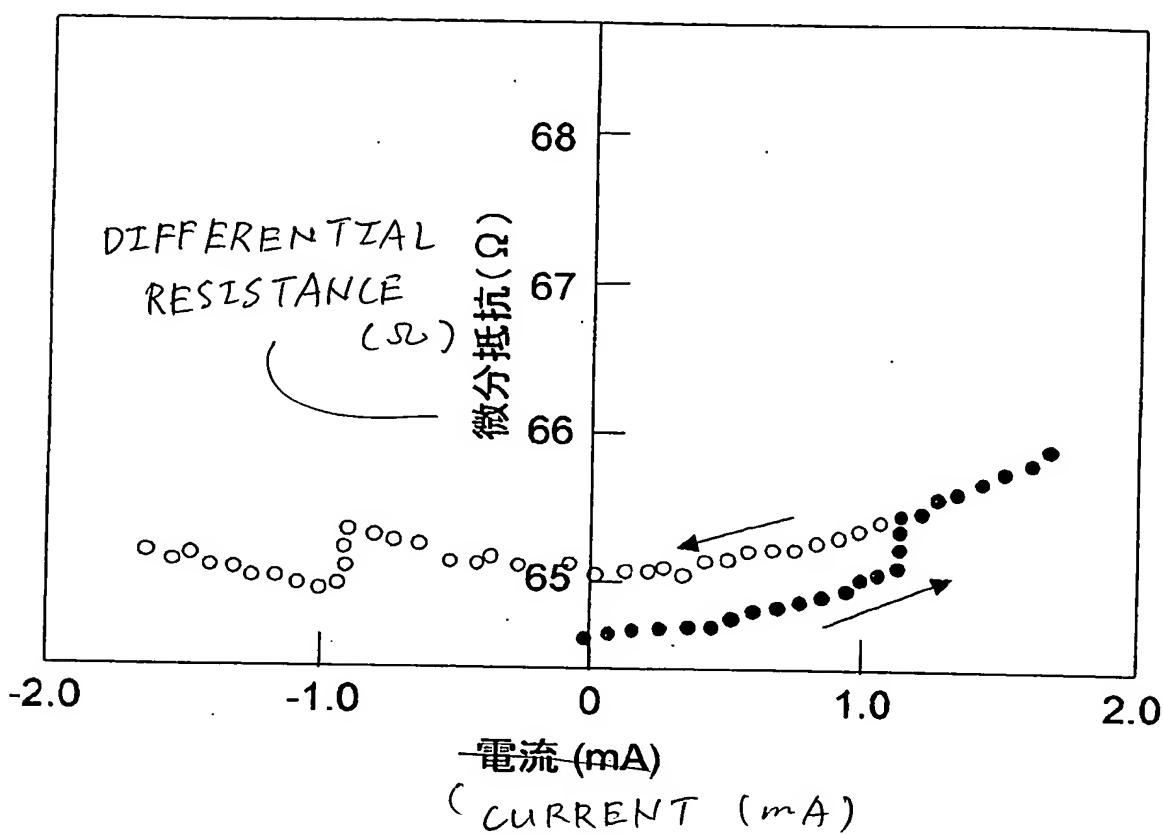
【図11】

FIG. 11



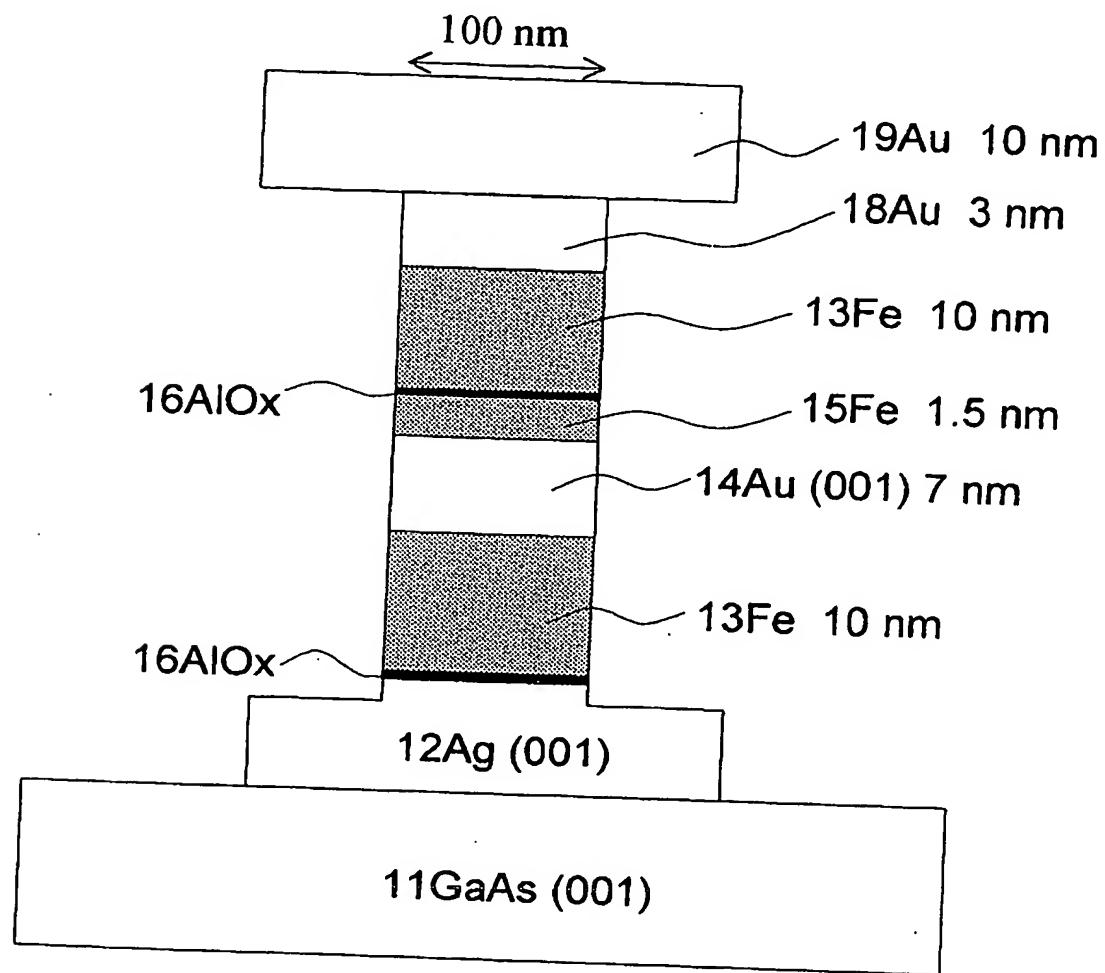
〔図12〕

FIG. 12



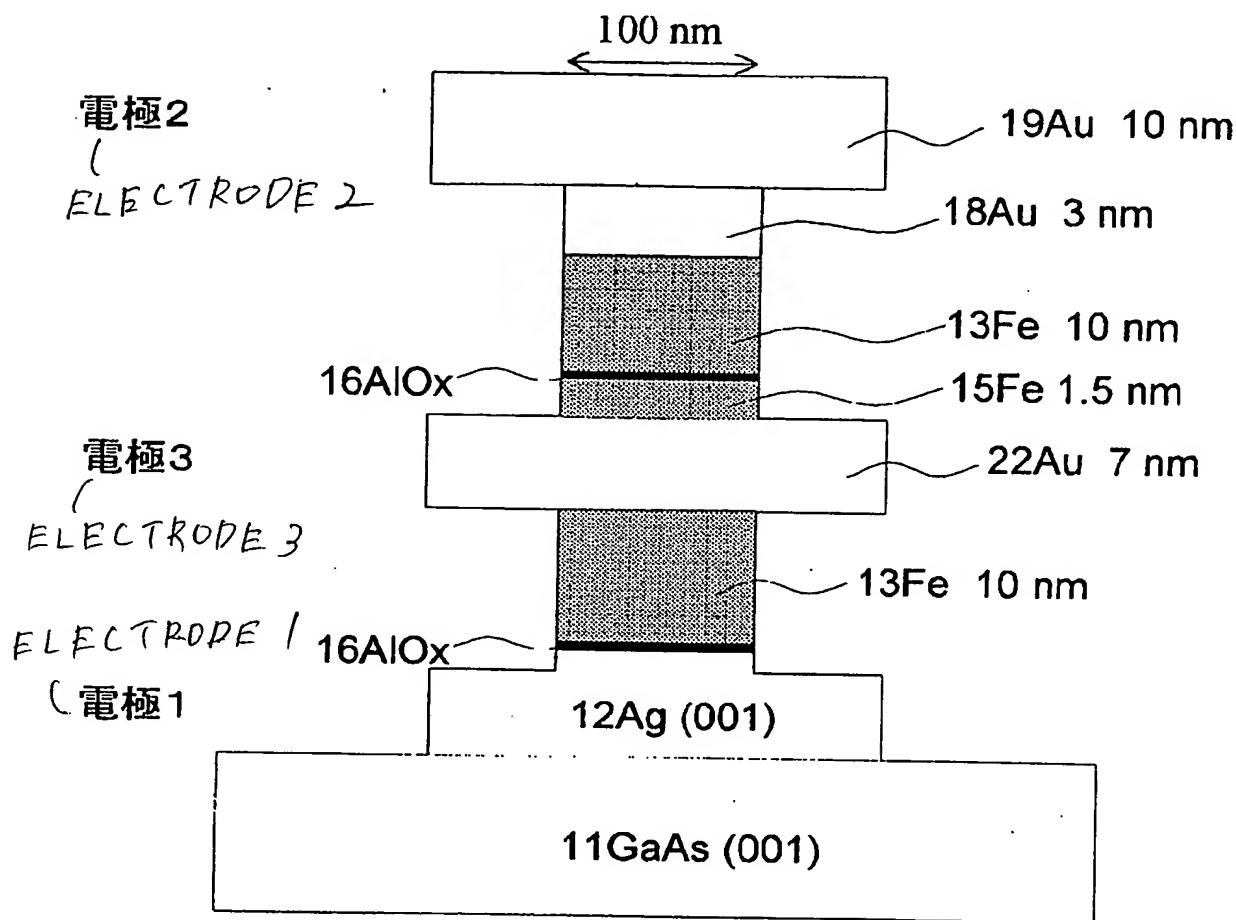
[図 13]

FIG. 13



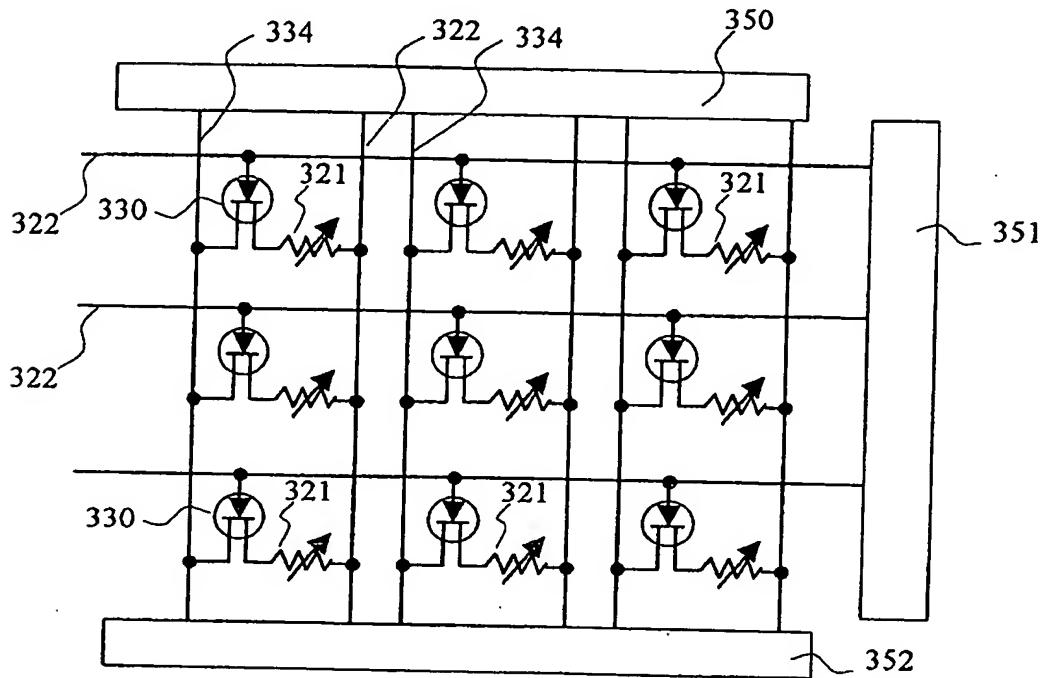
[図14]

FIG. 14.



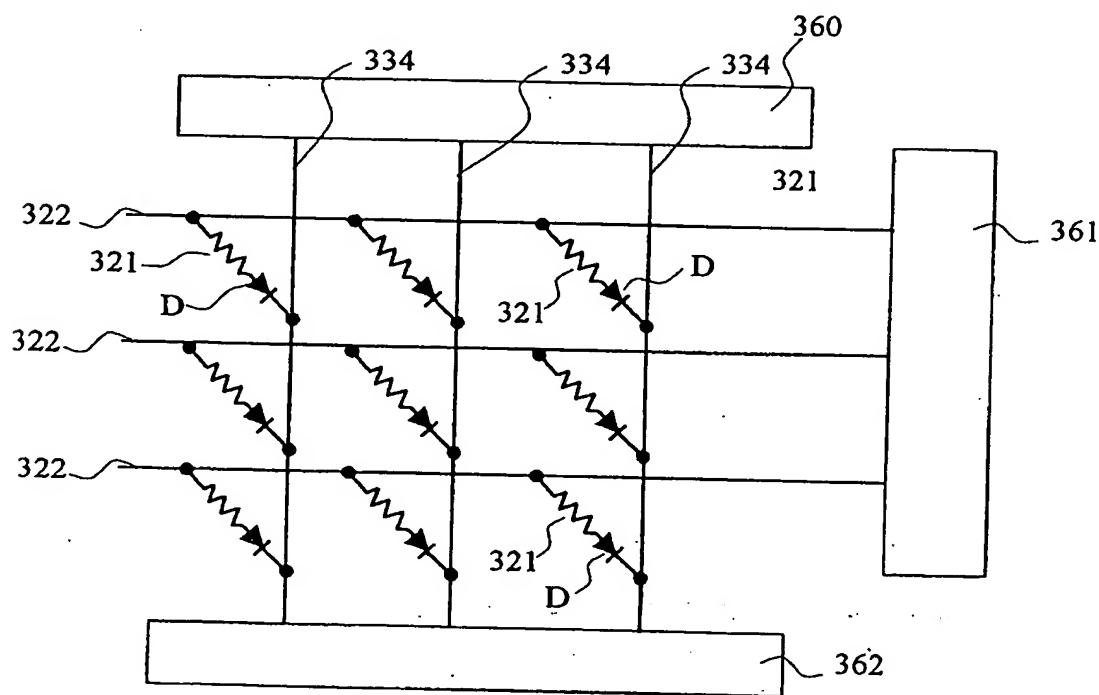
【図15】

FIG. 15



【図16】

FIG. 16



【図17】

FIG. 17

